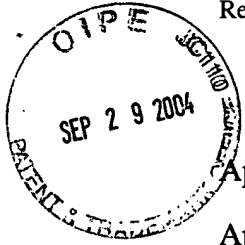


Application Serial No.: 10/683,853
Amendment dated September 27, 2004
Response to Office Action dated September 21, 2004



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Percival

Att'y Docket: 1348/110

App. No.: 10/683,853

Art Unit: 2186

Filing Date: October 10, 2003

Examiner: N/A

For: **Method and System for Coherently Caching
I/O Devices Across a Network**

Date: September 27, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 21, 2004

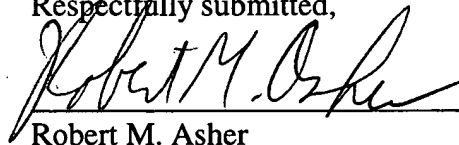
Dear Sir/Madam:

In response to the office action mailed September 21, 2004, Applicant submits herewith a terminal disclaimer to put the above-identified application in condition for allowance. Claims 1-17 were rejected under the judicially created doctrine of double patenting over claim 1 of U.S. Patent No. 6,370,615. The assignee for the present application and the parent patent is the same entity, Superspeed Software, Inc. (The examiner lists Massachusetts Institute of Technology in error) Applicant makes no comment as to the propriety of the rejection, but submits herewith a terminal disclaimer to expedite prosecution and remove the only ground for rejection raised by the Examiner.

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The present application is now in condition for allowance and early notice to that effect is respectfully solicited.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Robert M. Asher", written over a horizontal line.

Robert M. Asher
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